

<b>Notice of References Cited</b>	Application/Control No. 10/087,382	Applicant(s)/Patent Under Reexamination RADHAKRISHNA, HIREMANE S	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,681,261	01-2004	Mancusi et al.	709/250
	B	US-6,581,119	06-2003	Maeda et al.	710/260
	C	US-5,848,237	12-1998	Cross et al.	714/34
	D	US-4,573,118	02-1986	Damouny et al.	712/234
	E	US-3,676,861	07-1972	Ruth, Richard L.	710/262
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	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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	Q					
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	S					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"High speed data bus macro instruction set architecture" by Prohofsky, T. (abstract only)
	V	"Rapid migration to VLSI" by Lowinski, W.B.; Kirwan, R.; Perry, A.; Yu, T. (abstract only)
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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